Introduction to GLOBALFOUNDRIES 22 nm FDSOI technology

An introduction course on GLOBALFOUNDRIES 22 nm FDSOI technology presented by staff from GLOBALFOUNDRIES

The aim of this course is to enable designers of high performance integrated circuits to understand the design methodology for the complex and advanced FDSOI technology from GLOBALFOUNDRIES.

- 22 nm FDSOI introduction
- Analog & Mixed Signal "Best Practices"
- Reference flows
- Technology information and MPW submission for all GLOBALFOUNDRIES technologies

Course details:

Day 1

- Introduction to GLOBALFOUNDRIES and general overview of 22FDSOI technology
- Analog & Mixed Signal "Best Practices"
- Regular well and Flipped-well NFET/PFET body biasing
- Mismatch and corner simulations

Day 2

- Digital design flow and Synthesis with Cadence
- Layout constraints and Signoff
- RF components
- MPW participation procedure through Europractice
- Q&A, wrap up

Prerequisites: Course participants should be familiar with circuit and IC design in order to comprehend the presentation.

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04-Oct-2018 to 05-Oct-2018

09:00 to 17:00 (Local times)

To be held at Fraunhofer IIS, Am Wolfsmantel 33, 91058 Erlangen, Germany

Presented by GLOBALFOUNDRIES staff, Fraunhofer staff

This training course will accept bookings from Professors, Lecturers, Academic Staff and postgraduate students from established academic sites who are either Academic or Research Laboratory Members of EUROPRACTICE.

Only pre-booked and confirmed delegates may attend. Attendance is also subject to payment of course fees and signature of appropriate technology non-disclosure agreements and tool license agreements.

For further details and booking information please visit:

www.europractice.stfc.ac.uk