Press Release

Erlangen, December 2, 2011

Fraunhofer IIS and Fujitsu are Cooperating

In exchanging expertise and granting mutual access to their IP pools, Fujitsu Semiconductor Europe and the Fraunhofer Institute for Integrated Circuits IIS are facilitating the development of future-proof nanometer chips for their customers.

As part of this cooperation, new research and development projects have been under way since the beginning of 2010. These are aimed at multimedia, image processing and navigation applications in the automotive, industrial and telecommunications markets.

For this purpose, Fujitsu is making available its analogue and digital nanometer technology libraries, IP (intellectual property) pool and expertise. Fraunhofer IIS is complementing this with its own IP and decades of experience in IC and system design. Chip designs, right up to the GDSII data, are developed and evaluated together with customers. Fujitsu is producing prototypes in 90 nm and 65 nm CMOS technologies on multi-project wafers (MPW) at significantly reduced mask costs.

This means that the technology partners of Fujitsu and the IC developers at Fraunhofer IIS can implement fast and highly efficient solutions. There is potential for business models from prototypes (MPWs) via small series through to series production.
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“Industry, research and training all benefit from this co-operation. It grants cost-effective and dependable access to the latest nanometer technology for research projects, pilot series and products”, comments Josef Sauerer, Head of Integrated Circuits and Systems at Fraunhofer IIS.

Norbert Schuhmann, Digital Systems Group Manager, adds: “Particularly worth noting is the quality of the analogue and digital Fujitsu libraries, the easy access to their IP pool and their professional and smooth approach”.

Mark Ellins, Director for ASIC and Foundry Services at Fujitsu Semiconductor Europe said “We are very pleased to be able to work together with such an experienced design partner as Fraunhofer IIS, and believe our cooperation will provide significant benefits to developers requiring access to advanced process technologies”.

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About Fraunhofer IIS
Founded in 1985 the Fraunhofer Institute for Integrated Circuits IIS in Erlangen, today with more than 750 staff members, ranks first among the Fraunhofer Institutes concerning headcount and revenues. As the main inventor of mp3 and universally credited with the co-development of AAC audio coding standard, Fraunhofer IIS has reached worldwide recognition. It provides research services on contract basis and technology licensing.

The research topics are: Audio and video source coding, multimedia realtime systems, digital radio broadcasting and digital cinema systems, integrated circuits and sensor systems, design automation, wireless, wired and optical networks, localization and navigation, imaging systems and nanofocus X-ray technology, high-speed cameras, medical sensor solutions and supply chain services. The budget of more than 95 million Euro is mainly financed by projects from industry, the service sector and public authorities. Less than 25 percent of the budget is subsidized by federal and state funds.

Fujitsu Semiconductor Europe
Fujitsu Semiconductor Europe is a major supplier of semiconductor products. The company provides advanced systems solutions to the automotive, digital TV, mobile telephony, networking and industrial markets. Engineers from design centres dedicated to microcontrollers, graphics controllers, mixed-signal, wireless, multimedia ICs, ASIC products and software development, work closely with Fujitsu Semiconductor’s marketing and sales teams throughout EMEA to help satisfy customers’ systems development requirements. This solutions approach is supported by a broad range of advanced semiconductor devices, IP, building blocks and software.

For more information, please visit the company’s website at: http://emea.fujitsu.com/semiconductor